

Form A2-3: Compliance Verification Report for Type A Inverter Connected Power Generating Modules

This form should be used by the **Manufacturer** to demonstrate and declare compliance with the requirements of EREC G99. The form can be used in a variety of ways as detailed below:

1. To obtain **Fully Type Tested** status (≤ 50 kW)

The **Manufacturer** can use this form to obtain **Fully Type Tested** status for a **Power Generating Module** by registering this completed form with the Energy Networks Association (ENA) Type Test Verification Report Register. Tests 1 – 15 must all be completed and compliant for the **Power Generating Module** to be classified as **Fully Type Tested**.

2. To obtain **Type Tested** status for a product

This form can be used by the **Manufacturer** to obtain **Type Tested** status for a product which is used in a **Power Generating Module** by registering this form with the relevant parts completed with the Energy Networks Association (ENA) Type Test Verification Report Register.

Where the **Manufacturer** is seeking to obtain **Type Tested** status for an **Interface Protection** device the appropriate section of Form A2-4 should be used.

3. One-off Installation

This form can be used by the **Manufacturer** or **Installer** to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99. This form shall be submitted to the **DNO** as part of the application.

A combination of (2) and (3) can be used as required, together with Form A2-4 where compliance of the **Interface Protection** is to be demonstrated on site.

Note:

Within this Form A2-3 the term **Power Park Module** will be used but its meaning can be interpreted within Form A2-3 to mean **Power Park Module, Generating Unit or Inverter** as appropriate for the context. However, note that compliance shall be demonstrated at the **Power Park Module** level.

If the **Power Generating Module** is **Fully Type Tested** and registered with the Energy Networks Association (ENA) Type Test Verification Report Register, the Installation Document (Form A3-1 or A3-2) should include the **Manufacturer's** reference number (the system reference), and this form does not need to be submitted.

Where the **Power Generating Module** is not registered with the ENA Type Test Verification Report Register or is not **Fully Type Tested** this form (all or in parts as applicable) needs to be completed and provided to the **DNO**, to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99.

| | | | |
|--------------------------|----------------|--|---|
| PGM technology | | Hybrid Inverter SYNK-12K-SG04LP3 SYNK-10K-SG04LP3 SYNK-8K-SG04LP3 | |
| Manufacturer name | | SunSynk Ltd. | |
| Address | | Flat A, 3/F Wai Yip Industrial Building, 171 Wai Yip Street, Kwun Tong, Hong Kong | |
| Tel | +852 2884 4318 | Web site | http://www.sunsynk.com |

| | |
|--|-------------------------------|
| E:mail | kgoughuk@globaltech-china.com |
| Registered Capacity | 12kW/10kW/8kW |
| Energy storage capacity for Electricity Storage devices | 12kWh |

There are four options for Testing: (1) **Fully Type Tested**(≤ 50 kW), (2) **Type Tested** product, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type Tested PGMs** tests may be carried out at the time of commissioning (Form A4). **Type Tested** status is suitable for devices > 50 kW where the power quality aspects need consideration on a site by site basis in accordance with EREC G5 and EREC P28.
Insert Document reference(s) for **Manufacturers' Information**


| Tested option: | 1. Fully Type Tested | 2. Type Tested product | 3. One-off Manufacturers' Info. | 4. Tested on Site at time of Commissioning |
|---|----------------------|------------------------|---------------------------------|--|
| 0. Fully Type Tested - all tests detailed below completed and evidence attached to this submission | √ | N/A | N/A | N/A |
| 1. Operating Range | N/A | | | |
| 2. PQ – Harmonics | | | | |
| 3. PQ – Voltage Fluctuation and Flicker | | | | |
| 4. PQ – DC Injection (Power Park Modules only) | | | | |
| 5. Power Factor (PF) | | | | |
| 6. Frequency protection trip and ride through tests | | | | |
| 7. Voltage protection trip and ride through tests | | | | |
| 8. Protection – Loss of Mains Test, Vector Shift and RoCoF Stability Test | | | | |
| 9. LFSM-O Test | | | | |
| 10. Protection – Reconnection Timer | | | | |
| 11. Fault Level Contribution | | | | |
| 12. Self-monitoring Solid State Switch | | | | |

There are four options for Testing: (1) **Fully Type Tested**(≤ 50 kW), (2) **Type Tested** product, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type Tested PGMs** tests may be carried out at the time of commissioning (Form A4). **Type Tested** status is suitable for devices > 50 kW where the power quality aspects need consideration on a site by site basis in accordance with EREC G5 and EREC P28.

Insert Document reference(s) for **Manufacturers' Information**

| Tested option: | 1. Fully Type Tested | 2. Type Tested product | 3. One-off Manufacturers' Info. | 4. Tested on Site at time of Commissioning |
|---|----------------------|------------------------|---------------------------------|--|
| 13. Wiring functional tests if required by para 15.2.1 (attach relevant schedule of tests) | | | | |
| 14. Logic Interface (input port) | | | | |
| 15. Cyber security | | | | |

Manufacturer compliance declaration. - I certify that all products supplied by the company with the above **Type Tested Manufacturer's** reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site **Modifications** are required to ensure that the product meets all the requirements of EREC G99.

| | | | |
|--------|--|--------------|--------------|
| Signed |  | On behalf of | SunSynk Ltd. |
|--------|--|--------------|--------------|

Note that testing can be done by the **Manufacturer** of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.

A2-3 Compliance Verification Report –Tests for Type A Inverter Connected Power Generating Modules – test record

1. Operating Range: Tests should be carried with the **Power Generating Module** operating at **Registered Capacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within $\pm 5\%$ of the apparent power value set for the entire duration of each test sequence.

Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.

The **Interface Protection** shall be disabled during the tests.

In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source.

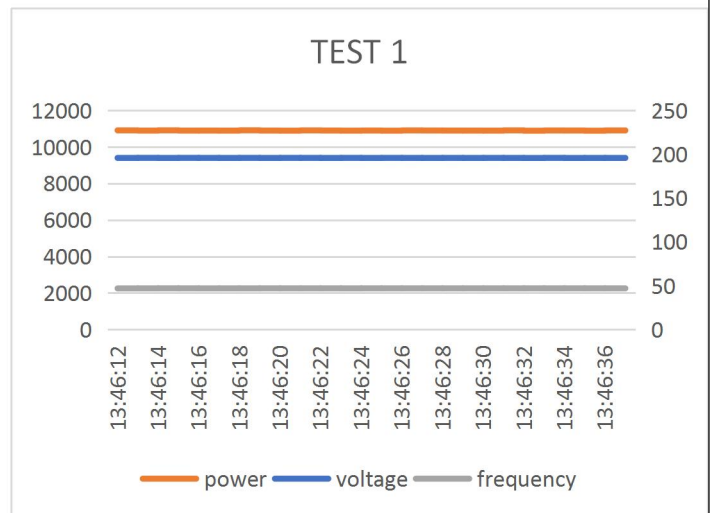
In case of a full converter **Power Park Module** (eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a DC source.

Pass or failure of the test should be indicated in the fields below (right hand side), for example with the statement "Pass", "No disconnection occurs", etc. Graphical evidence is preferred.

Note that the value of voltage stated in brackets assumes a **LV** connection. This should be adjusted for **HV** as required.

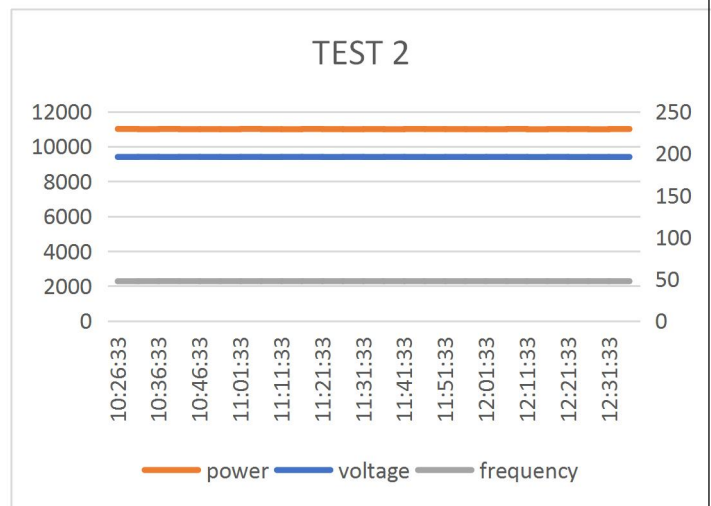
Test 1

Voltage = 85% of nominal (195.5 V),
 Frequency = 47 Hz,
Power Factor = 1,
 Period of test 20 s

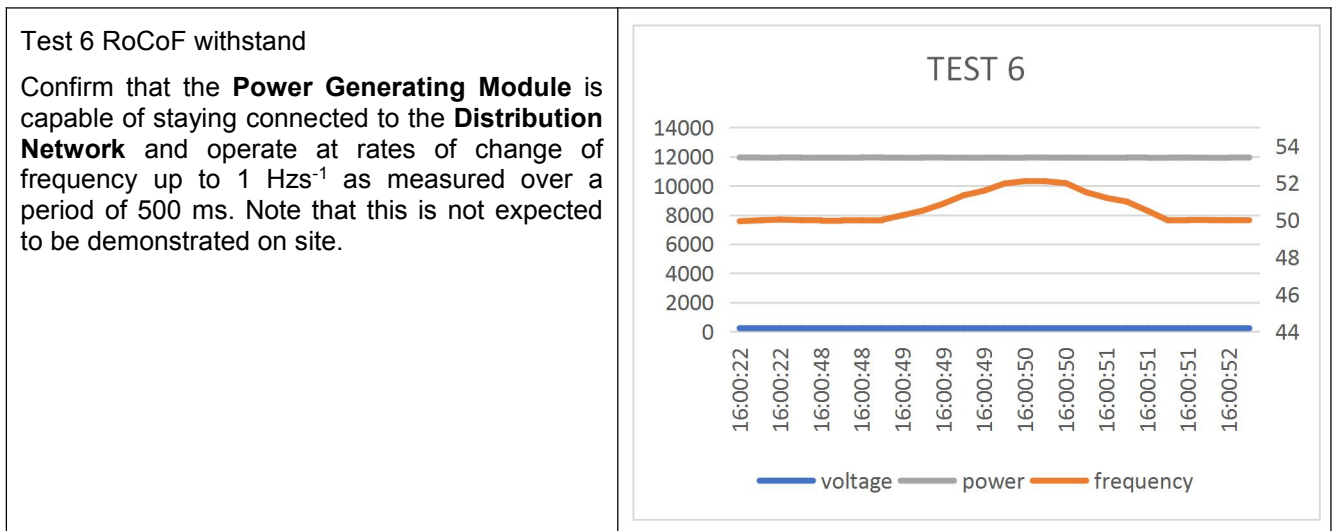


Test 2

Voltage = 85% of nominal (195.5 V),
 Frequency = 47.5 Hz,
Power Factor = 1,
 Period of test 90 minutes



| | |
|--|---|
| <p>Test 3</p> <p>Voltage = 110% of nominal (253 V), Frequency = 51.5 Hz, Power Factor = 1, Period of test 90 minutes</p> | <p>TEST 3</p> <p>14000 12000 10000 8000 6000 4000 2000 0</p> <p>300 250 200 150 100 50 0</p> <p>18:03:02 18:13:02 18:23:02 18:33:02 18:43:02 18:53:02 19:03:02 19:13:02 19:23:02 19:33:02 19:43:02 19:53:02 20:03:02</p> <p>power voltage frequency</p> |
| <p>Test 4</p> <p>Voltage = 110% of nominal (253 V), Frequency = 52.0 Hz, Power Factor = 1, Period of test 15 minutes</p> | <p>TEST 4</p> <p>14000 12000 10000 8000 6000 4000 2000 0</p> <p>300 250 200 150 100 50 0</p> <p>8:12:55 8:14:55 8:16:55 8:18:55 8:20:55 8:22:55 8:24:55 8:26:55 8:28:55 8:30:55 8:32:55 8:34:55 8:36:55</p> <p>power voltage frequency</p> |
| <p>Test 5</p> <p>Voltage = 100% of nominal (230 V), Frequency = 50.0 Hz, Power Factor = 1, Period of test = 90 minutes</p> | <p>PASS</p> |



2. Power Quality – Harmonics:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12, and measurements for the 2nd – 13th harmonics should be provided. The results need to comply with the limits of Table 2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment. For three phase **Power Generating Modules**, measurements for all phases should be provided.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC G5.

The rating of the **Power Generating Module** (per phase) should be provided below, and the Total Harmonic Distortion (THD) and Partial Weighted Harmonic Distortion (PWhd) should be provided at the bottom of this section.

| | | | | | | | | |
|---|---|------|------|--------------------------|------|---|---------------------------|------------|
| Power Generating Module tested to BS EN 61000-3-12 | | | | | | | | |
| Power Generating Module rating per phase (rpp) | 4 | | | | kVA | Harmonic % = Measured Value (A) x 23/rating per phase (kVA) | | |
| Single or three phase measurements (for single phase measurements, only complete L1 columns below). | | | | | | | | |
| Harmonic | At 45-55% of Registered Capacity | | | | | | Limit in BS EN 61000-3-12 | |
| | Measured Value (MV) in Amps | | | Measured Value (MV) in % | | | | |
| | L1 | L2 | L3 | L1 | L2 | L3 | 1 phase | 3 phase |
| 2 | 0.04 | 0.08 | 0.05 | 0.46 | 0.92 | 0.57 | 8% | 8% |
| 3 | 0.05 | 0.04 | 0.02 | 0.57 | 0.46 | 0.23 | 21.6% | Not stated |
| 4 | 0.03 | 0.04 | 0.06 | 0.34 | 0.46 | 0.69 | 4% | 4% |
| 5 | 0.12 | 0.09 | 0.09 | 1.38 | 1.03 | 1.03 | 10.7% | 10.7% |

| | | | | | | | | |
|-------------------|---------------------------------------|------|------|--------------------------|------|------|---------------------------|------------|
| 6 | 0.02 | 0.05 | 0.03 | 0.23 | 0.57 | 0.34 | 2.67% | 2.67% |
| 7 | 0.16 | 0.15 | 0.16 | 1.84 | 1.72 | 1.84 | 7.2% | 7.2% |
| 8 | 0.02 | 0.02 | 0.02 | 0.23 | 0.23 | 0.23 | 2% | 2% |
| 9 | 0.00 | 0.01 | 0.02 | 0.00 | 0.11 | 0.23 | 3.8% | Not stated |
| 10 | 0.02 | 0.03 | 0.02 | 0.23 | 0.34 | 0.23 | 1.6% | 1.6% |
| 11 | 0.00 | 0.01 | 0.01 | 0.00 | 0.11 | 0.11 | 3.1% | 3.1% |
| 12 | 0.01 | 0.01 | 0.01 | 0.11 | 0.11 | 0.11 | 1.33% | 1.33% |
| 13 | 0.03 | 0.03 | 0.04 | 0.34 | 0.34 | 0.46 | 2% | 2% |
| THD ¹ | | | | 2.89 | 3.12 | 3.91 | 23% | 13% |
| PWHD ² | | | | 6.12 | 6.73 | 7.38 | 23% | 22% |
| Harmonic | At 100% of Registered Capacity | | | | | | Limit in BS EN 61000-3-12 | |
| | Measured value (MV) in Amps | | | Measured value (MV) in % | | | | |
| | L1 | L2 | L3 | L1 | L2 | L3 | 1 phase | 3 phase |
| 2 | 0.06 | 0.06 | 0.04 | 0.34 | 0.34 | 0.23 | 8% | 8% |
| 3 | 0.05 | 0.06 | 0.07 | 0.29 | 0.34 | 0.40 | 21.6% | Not stated |
| 4 | 0.02 | 0.05 | 0.06 | 0.11 | 0.29 | 0.34 | 4% | 4% |
| 5 | 0.45 | 0.45 | 0.46 | 2.59 | 2.59 | 2.64 | 10.7% | 10.7% |
| 6 | 0.02 | 0.02 | 0.05 | 0.11 | 0.11 | 0.29 | 2.67% | 2.67% |
| 7 | 0.31 | 0.31 | 0.31 | 1.78 | 1.78 | 1.78 | 7.2% | 7.2% |
| 8 | 0.02 | 0.03 | 0.01 | 0.11 | 0.17 | 0.06 | 2% | 2% |
| 9 | 0.01 | 0.02 | 0.01 | 0.06 | 0.11 | 0.06 | 3.8% | Not stated |
| 10 | 0.03 | 0.02 | 0.03 | 0.17 | 0.11 | 0.17 | 1.6% | 1.6% |
| 11 | 0.15 | 0.13 | 0.13 | 0.86 | 0.75 | 0.75 | 3.1% | 3.1% |
| 12 | 0.00 | 0.02 | 0.01 | 0.00 | 0.11 | 0.06 | 1.33% | .33% |
| 13 | 0.09 | 0.10 | 0.10 | 0.52 | 0.57 | 0.57 | 2% | 2% |

¹ THD = Total Harmonic Distortion

² PWHD = Partial Weighted Harmonic Distortion

| | | | | | | | | |
|-------------------|--|--|--|------|------|------|-----|-----|
| THD ³ | | | | 1.94 | 2.03 | 2.25 | 23% | 13% |
| PWHD ⁴ | | | | 4.62 | 4.98 | 5.33 | 23% | 22% |

3. Power Quality – Voltage fluctuations and Flicker:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC P28.

The standard test impedance is 0.4 Ω for a single phase **Power Generating Module** (and for a two phase unit in a three phase system) and 0.24 Ω for a three phase **Power Generating Module** (and for a two phase unit in a split phase system). Please ensure that both test and standard impedance are completed on this form. If the test impedance (or the measured impedance) is different to the standard impedance, it must be normalised to the standard impedance as follows (where the **Power Factor** of the generation output is 0.98 or above):

$d_{\text{max normalised value}} = (\text{Standard impedance} / \text{Measured impedance}) \times \text{Measured value}.$

Where the **Power Factor** of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the standard impedance.

The stopping test should be a trip from full load operation.

The duration of these tests needs to comply with the particular requirements set out in the testing notes for the technology under test.

The test date and location must be declared.

| | | | | | | | | |
|--|--|------|------|---------------|-----------|------|---------|--------------|
| Test start date | 2021.5.27 | | | Test end date | 2021.5.27 | | | |
| Test location | No.26 South Yongjiang Road, Daqi, Beilun, NingBo, China. | | | | | | | |
| | Starting | | | Stopping | | | Running | |
| | d max | d c | d(t) | d max | d c | d(t) | P st | P lt 2 hours |
| Measured Values at test impedance | 0.08 | 0.42 | 0.0 | 0.1 | 0.61 | 0 | 0.29 | 0.16 |
| Normalised to standard impedance | 0.08 | 0.42 | 0.0 | 0.1 | 0.61 | 0 | 0.29 | 0.16 |
| Normalised to required maximum impedance | - | - | - | - | - | - | - | - |

³ THD = Total Harmonic Distortion

⁴ PWHD = Partial Weighted Harmonic Distortion

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| | | | | | | | | |
|-----------------------------------|----|------|------|----|------|------|-----|------|
| Limits set under BS EN 61000-3-11 | 4% | 3.3% | 3.3% | 4% | 3.3% | 3.3% | 1.0 | 0.65 |
|-----------------------------------|----|------|------|----|------|------|-----|------|

| Test Impedance | R | 0.24 | Ω | XI | 0.15 | Ω |
|---|-------------------|------------|--------------|------------|-----------------|-----------------|
| Standard Impedance | R | 0.24 * | Ω | XI | 0.15 * | Ω |
| Maximum Impedance | R | - | Ω | XI | - | Ω |
| <p>* Applies to three phase and split single phase Power Generating Modules. Delete as appropriate.</p> <p>^ Applies to single phase Power Generating Module and Power Generating Modules using two phases on a three phase system. Delete as appropriate.</p> | | | | | | |
| <p>4. Power quality – DC injection: The tests should be carried out on a single Generating Unit. Tests are to be carried out at three defined power levels ±5%. At 230 V a 50 kW three phase Inverter has a current output of 217 A so DC limit is 543 mA. These tests should be undertaken in accordance with Annex A.7.1.4.4.</p> <p>The % DC injection (“as % of rated AC current” below) is calculated as follows:</p> <p>% DC injection = Recorded DC value in Amps / Base current</p> <p>where the base current is the Registered Capacity (W) / Vphase. The % DC injection should not be greater than 0.25%.</p> | | | | | | |
| Test power level | 10% | | 55% | | 100% | |
| Recorded DC value in Amps | 12.2mA | | 22.5mA | | 23.8mA | |
| as % of rated AC current | 0.023% | | 0.043% | | 0.046% | |
| Limit | 0.25% | | 0.25% | | 0.25% | |
| <p>5. Power Factor: The tests should be carried out on a single Power Generating Module. Tests are to be carried out at three voltage levels and at Registered Capacity and the measured Power Factor must be greater than 0.95 to pass. Voltage to be maintained within ±1.5% of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2.</p> <p>Note that the value of voltage stated in brackets assumes a LV connection. This should be adjusted for HV as required.</p> | | | | | | |
| Voltage | 0.94 pu (216.2 V) | | 1 pu (230 V) | | 1.1 pu (253 V) | |
| Measured value | 0.999 | | 0.999 | | 0.999 | |
| Power Factor Limit | >0.95 | | >0.95 | | >0.95 | |
| <p>6. Protection – Frequency tests: These tests should be carried out in accordance with the Annex A.7.1.2.3. For trip tests, frequency and time delay should be stated. For “no trip tests”, “no trip” can be stated.</p> | | | | | | |
| Function | Setting | | Trip test | | “No trip tests” | |
| | Frequency | Time delay | Frequency | Time delay | Frequency /time | Confirm no trip |
| U/F stage 1 | 47.5 Hz | 20 s | 47.51 | 20.02s | 47.7 Hz | no trip |

| | | | | | | |
|-------------|-------|-------|----------|--------|--------------------|---------|
| | | | Hz | | 30 s | |
| U/F stage 2 | 47 Hz | 0.5 s | 47.01 Hz | 0.535s | 47.2 Hz 19.5 s | no trip |
| | | | | | 46.8 Hz 0.45 s | no trip |
| O/F | 52 Hz | 0.5 s | 52Hz | 0.515s | 51.8 Hz 120.0 s | no trip |
| | | | | | 52.2 Hz 0.45 s | no trip |

Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the protection can be used. The “No trip tests” need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7. Protection – Voltage tests: These tests should be carried out in accordance with Annex A.7.1.2.2. For trip tests, voltage and time delay should be stated. For “no trip tests”, “no trip” can be stated.

Note that the value of voltage stated below assumes a **LV** connection This should be adjusted for **HV** taking account of the VT ratio as required.

| Function | Setting | | Trip test | | “No trip tests” | |
|-------------|-------------------|------------|-----------|------------|-------------------|-----------------|
| | Voltage | Time delay | Voltage | Time delay | Voltage /time | Confirm no trip |
| U/V | 0.8 pu (184 V) | 2.5 s | 184V | 2.53s | 188 V 5.0 s | no trip |
| | | | | | 180 V 2.45 s | no trip |
| O/V stage 1 | 1.14 pu (262.2 V) | 1.0 s | 262V | 1.031s | 258.2 V 5.0 s | no trip |
| O/V stage 2 | 1.19 pu (273.7 V) | 0.5 s | 273.7V | 0.522s | 269.7 V 0.95 s | no trip |
| | | | | | 277.7 V 0.45 s | no trip |

Note for Voltage tests the Voltage required to trip is the setting ± 3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

| | | | | | | |
|---|-------------------------------------|-------------------------|-------------------------|-------------------------|------------------------------|--------------------------|
| 8. Protection – Loss of Mains test: These tests should be carried out in accordance with BS EN 62116. Annex A.7.1.2.4. | | | | | | |
| The following sub set of tests should be recorded in the following table. | | | | | | |
| Test Power and imbalance | 33% -5% Q Test 22 | 66% -5% Q Test 12 | 100% -5% P Test 5 | 33% +5% Q Test 31 | 66% +5% Q Test 21 | 100% +5% P Test 10 |
| Trip time. Limit is 0.5s ⁵ | 0.159s | 0.232s | 0.355s | 0.188s | 0.298s | 0.401s |
| Loss of Mains Protection, Vector Shift Stability test: This test should be carried out in accordance with Annex A.7.1.2.6. Confirmation is required that the Power Generating Module does not trip under positive / negative vector shift. | | | | | | |
| | Start Frequency | Change | Confirm no trip | | | |
| Positive Vector Shift | 49.5 Hz | +50 degrees | no trip | | | |
| Negative Vector Shift | 50.5 Hz | - 50 degrees | no trip | | | |
| Loss of Mains Protection, RoCoF Stability test: This test should be carried out in accordance with Annex A.7.1.2.6. Confirmation is required that the Power Generating Module does not trip for the duration of the ramp up and ramp down test. | | | | | | |
| Ramp range | Test frequency ramp: | | Test Duration | | Confirm no trip | |
| 49.0 Hz to 51.0 Hz | +0.95 Hzs ⁻¹ | | 2.1 s | | no trip | |
| 51.0 Hz to 49.0 Hz | -0.95 Hzs ⁻¹ | | 2.1 s | | no trip | |
| 9. Limited Frequency Sensitive Mode – Overfrequency test: The test should be carried out using the specific threshold frequency of 50.4 Hz and Droop of 10%. This test should be carried out in accordance with Annex A.7.1.3, which also contains the measurement tolerances. | | | | | | |
| Active Power response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex A.7.2.4. | | | | | Y/N | |
| Alternatively, test results should be noted below: | | | | | | |
| Test sequence at Registered Capacity >80% | Measured Active Power Output | Frequency | Primary Power Source | | Active Power Gradient | |
| Step a) 50.00Hz ±0.01Hz | 12005 | 50.00 | 12330W | | NA | |

⁵ If the device requires additional shut down time (beyond 0.5 s but less than 1 s) then this should be stated on this form.

| | | | | |
|--|-------------------------------------|------------------|-----------------------------|------------------------------|
| Step b) 50.45Hz ±0.05Hz | 11876 | 50.45 | | - |
| Step c) 50.70Hz ±0.10Hz | 11280 | 50.70 | | - |
| Step d) 51.15Hz ±0.05Hz | 10202 | 51.15 | | - |
| Step e) 50.70Hz ±0.10Hz | 11283 | 50.70 | | - |
| Step f) 50.45Hz ±0.05Hz | 11869 | 50.45 | | - |
| Step g) 50.00Hz ±0.01Hz | 120002 | 50.00 | | |
| Test sequence at Registered Capacity 40% - 60% | Measured Active Power Output | Frequency | Primary Power Source | Active Power Gradient |
| Step a) 50.00Hz ±0.01Hz | 6000 | 50.00 | 6185W | - |
| Step b) 50.45Hz ±0.05Hz | 5881 | 50.45 | | - |
| Step c) 50.70Hz ±0.10Hz | 5275 | 50.70 | | - |
| Step d) 51.15Hz ±0.05Hz | 4203 | 51.15 | | - |
| Step e) 50.70Hz ±0.10Hz | 5279 | 50.70 | | - |
| Step f) 50.45Hz ±0.05Hz | 5883 | 50.45 | | |
| Step g) 50.00Hz ±0.01Hz | 6005 | 50.00 | | |

| 10. Protection – Re-connection timer | | | | | |
|--|----------------|--|--|-----------------|-----------------|
| Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 10.1. Both the time delay setting and the measured delay should be provided in this form; both should be greater than 20 s to pass. Confirmation should be provided that the Power Generating Module does not reconnect at the voltage and frequency settings below; a statement of “no reconnection” can be made. | | | | | |
| Time delay setting | Measured delay | Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of Table 10.1. | | | |
| 60S | 65S | At 1.16 pu (266.2 V LV connection, 127.6 V HV connection assuming 110 V ph-ph VT) | At 0.78 pu (180.0 V LV connection, 85.8 V HV connection assuming 110 V ph-ph VT) | At 47.4 Hz | At 52.1 Hz |
| Confirmation that the Power Generating Module does not re-connect. | | No reconnection | No reconnection | No reconnection | No reconnection |
| 11. Fault level contribution: These tests shall be carried out in accordance with EREC G99 Annex A.7.1.5. Please complete each entry, even if the contribution to the fault level is zero. | | | | | |
| For Inverter output | | | | | |
| Time after fault | Volts | Amps | | | |
| 20ms | 217V | 360mA | | | |
| 100ms | NA | NA | | | |
| 250ms | NA | NA | | | |
| 500ms | NA | NA | | | |
| Time to trip | 0.08 | In seconds | | | |
| 12. Self-Monitoring solid state switching: No specified test requirements. Refer to Annex A.7.1.6. | | | | | |
| It has been verified that in the event of the solid state switching device failing to disconnect the Power Park Module , the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s. | | | | | NA |
| 13. Wiring functional tests: If required by para 15.2.1. | | | | | |
| Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning) | | | | | NA |

| 14. Logic interface (input port) | |
|--|-----|
| Confirm that an input port is provided and can be used to shut down the module | Yes |
| Provide high level description of logic interface, e.g. details in 11.1.3.1 such as AC or DC signal (the additional comments box below can be used) | Yes |
| 15. Cyber security | |
| Confirm that the Power Generating Module has been designed to comply with cyber security requirements, as detailed in 9.1.7. | Yes |
| Additional comments. | |
| <p>1. Product have the same circuit and construction, only the output power are different and controlled by software, And the test result can refer to the test model.</p> <p>2. The inverter is equipped with Modbus interface, Supports remote monitoring and parameter modification; Connect the battery BMS and Connect meter.</p> <p>3. The inverter has an RJ45 interface that accepts signals from the DNO, the RJ45 logic interface will take the form of a simple binary output, When the signal is 0, the inverter works normally, When the signal is 1, the inverter will reduce its Active Power to zero within 5 s.</p> | |